

AN $N \log N$ ALGORITHM FOR MINIMIZING
STATES IN A FINITE AUTOMATON

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Abstract

An algorithm is given for minimizing the number of states in a finite automaton or for determining if two finite automata are equivalent. The asymptotic running time of the algorithm is bounded by $kn \log n$ where k is some constant and n is the number of states. The constant k depends linearly on the size of the input alphabet.

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Introduction

Most basic texts on finite automata give algorithms for minimizing the number of states in a finite automaton [1, 2]. However, a worst case analysis of these algorithms indicate that they are n^2 processes where n is the number of states. For finite automata with large numbers of states, these algorithms are grossly inefficient. Thus in this paper we describe an algorithm for minimizing the states in which the asymptotic running time in a worst case analysis grows as $n \log n$. The constant of proportionality depends linearly on the number of input symbols. Clearly the same algorithm can be used to determine if two finite automata are equivalent.

The essence of previously published algorithms was to first partition the states according to their outputs. The blocks of the partitions are then repeatedly refined by examining the successor state on a given input for each state in the block. States whose successor states on a given input are in different blocks are placed in separate blocks. When no further refinement is possible, all states in the same block of the partition can be shown to be equivalent. Consider the example in Figure 1. The initial partition is $(1,2,3,4,5)(6)$. Since on input 0, the successor

states of states 1, 2, 3 and 4 are in the first block of the partition and the successor of state 5 is in the second block, the first iteration refines the partition into the blocks $(1,2,3,4)(5)(6)$. Successive refinements yield $(1,2,3)(4)(5)(6)$; $(1,2)(3)(4)(5)(6)$ and $(1)(2)(3)(4)(5)(6)$. Thus, in this example all pairs of states are inequivalent.

State	Input		Output
	0	1	
1	2	1	0
2	3	2	0
3	4	3	0
4	5	4	0
5	6	5	0
6	6	6	1

Next State

Figure 1

For this example it is seen that as many as n iterations may be required and the total number of steps needed to execute the algorithm if implemented in a straightforward fashion on a digital computer is n^2 .

The algorithm proposed in this paper may also require n iterations but the work per iteration summed over all iterations yields only $n \log n$. We illustrate the algorithm by an example before specifying it in detail. Extensive use of list processing is employed to reduce the computation time. First the state table is inverted to obtain the table shown in Figure 2. The states are partitioned according to their outputs $(1,2,3,4,5)(6)$. Next a block and an input symbol on which the partition is refined are selected. Assume the block (6) and input 0 are selected. The states in each block are further partitioned depending on whether on input 0 their next state is in block (6) or not. Thus the next partition is $(1,2,3,4)(5)(6)$. Note that had we partitioned on the block $(1,2,3,4,5)$ and input 0 we would have obtained the same result.

More generally, once we have partitioned on a block and an input symbol, we need never partition on that block and input symbol again until the block is split and then we need only partition on one of the two subblocks. Since the time needed to partition on a block is proportional to the transitions into the block and since we can always select the half with fewer transitions, the total number of steps in the algorithm is bounded by $n \log n$.

States	Input		Output
	0	1	
1	-	1	0
2	1	2	0
3	2	3	0
4	3	4	0
5	4	5	0
6	5,6	6	1

previous state

Figure 2

Formal description of the algorithm

Let $A = (S, I, \delta, F)$ be a finite automaton where S is a finite set of states, I is a finite set of inputs, δ is a mapping from $S \times I$ into S and $F \subseteq S$ is the set of final states. No initial state is specified since it is of no importance in what follows. The mapping δ is extended to $S \times I^*$ in the usual manner where I^* denotes the set of all finite length strings of symbols from I . States s and t are said to be equivalent if for each x in I^* , $\delta(s, x)$ is in F if and only if $\delta(t, x)$ is in F .

The algorithm for finding the equivalence classes of S is described below.

Step 1. $\forall s \in S$ and $a \in I$ construct

$$\delta^{-1}(s, a) = \{t \mid \delta(t, a) = s\}.$$

Step 2. Construct $B(1) = F$, $B(2) = S - F$ and for each a in I and $1 \leq i \leq 2$ construct

$$a(i) = \{s \mid s \in B(i) \text{ and } \delta^{-1}(s, a) \neq \emptyset\}.$$

Step 3. Set $k = 3$

Step 4. $\forall a \in I$ construct

$$L(a) = \begin{cases} \{1\} & \text{if } |a(1)| \leq |a(2)| \\ \{2\} & \text{otherwise} \end{cases}$$

Step 5. Select a in I and i in $L(a)$. The algorithm terminates when $L(a) = \emptyset$ for each a in I .

Step 6. Delete i from $L(a)$.

Step 7. $\forall j < k$ st $\exists t$ in $B(j)$ with $\delta(t, a) \in a(i)$ perform steps 7a, 7b, 7c, and 7d.

Step 7a. Partition $B(j)$ into $B'(j) = \{t \mid \delta(t, a) \in a(i)\}$ and $B''(j) = B(j) - B'(j)$.

Step 7b. Replace $B(j)$ by $B'(j)$ and construct $B(k) = B''(j)$. Construct corresponding $a(j)$ and $a(k)$ for each a in I .

Step c. $\forall a \in I$ modify $L(a)$ as follows.

$$L(a) = \begin{cases} L(a) \cup \{j\} & \text{if } j \notin L(a) \text{ and } 0 < |a(j)| \leq |a(k)| \\ L(a) \cup \{k\} & \text{otherwise} \end{cases}$$

Step 7d. Set $k = k+1$.

Step. Return to Step 5.

Correctness of algorithm

The claim is made that on termination of the algorithm two states are equivalent if and only if they are in the same block. The algorithm must terminate since the only times that an index is added to $L(a)$ for some a in I are in Step 4 which is executed only once and in Step 7c. An index is added at Step 7c only after a refinement of a block of the partition. Each time Step 6 is executed, an index is removed from $L(a)$ for some a . Thus the algorithm must terminate.

It is easily shown by induction on the number of times Step 7a is executed that if s is in $B(i)$ and t is in $B(j)$, $i \neq j$, then s is not equivalent to t . Clearly, it is true the first time Step 7a is executed since only two blocks exist, $B(1)$ containing only final states and $B(2)$ containing only nonfinal states. Blocks are refined at Step 7a only when successor states on a given input have previously been shown to be inequivalent.

To see that two inequivalent states cannot be in the same block when the algorithm terminates, assume that states s and t are in $B(i)$ and that s and t are not equivalent. Without loss of generality, assume $\delta(s, a)$ is in $B(j)$ and $\delta(t, a)$ is in $B(k)$ where $j \neq k$. (If $\delta(s, a)$ and $\delta(t, a)$ are in the same block then there exists a shortest x such that $\delta(s, x)$ and $\delta(t, x)$ are in distinct blocks. Clearly an x exists and hence a shortest x since for some x one or the other of $\delta(s, x)$ and $\delta(t, x)$ but not both is in a final state and each block consists solely of final or solely of nonfinal states. Let a be the last symbol of x and write $x = ya$. Then $\delta(s, y)$ and $\delta(t, y)$ are in the same block, $\delta(s, y)$ and $\delta(t, y)$ are not equivalent and $\delta(\delta(s, y), a)$ and $\delta(\delta(t, y), a)$ are in different blocks. Replace s by $\delta(s, y)$ and replace t by $\delta(t, y)$.) Consider the point at which the block containing $\delta(s, a)$ and $\delta(t, a)$ was partitioned so that $\delta(s, a)$ and $\delta(t, a)$ first appeared in separate subblocks. At that point one of the two subblocks was placed in $L(a)$. When this subblock is removed from $L(a)$, the block containing s and t is partitioned with s and t going into separate subblocks. Thus s and t cannot both be in $B(i)$, a contradiction.

Analysis of the running time

The running time of the algorithm is clearly dependent on the implementation. The algorithm has been programmed in ALGOL. Since the implementation consists of approximately 300 ALGOL statements we shall simply indicate how the various steps were implemented and discuss informally their running time.

The sets such as $\delta^{-1}(s,a)$, $L(a)$, etc. were represented by linked lists in such a way that an item could be added or deleted at the beginning of the list in a fixed number of steps. Vectors were also maintained to indicate if a state was or was not on a given list. This eliminates searching a list simply to determine if the item is on the list and is essential in Step 7c. The sets $B(i)$ and $a(i)$ were represented as doubly linked lists so that an item could be added or deleted anywhere in the list in a fixed number of steps once the position is given. The structure was such that given a state s , its position in $B(i)$ and $a(i)$ could be determined in a fixed number of steps.

Steps 1, 2, and 4 are executed only once and require time proportional to the product of the number of states times the number of input symbols. Steps 5 through 8 form a simple loop. The time necessary to traverse the loop for given a in I and i in $L(a)$ is proportional to the number of state transitions on input a terminating on states in $B(i)$. (To see this, note that Steps 5, 6 and 8 are finite. In Step 7 we do not need to examine $B(j)$ for each $j < k$ - i.e. see if there exists a t in $B(j)$ with $\delta(t,a)$ in $a(i)$. Rather we look at each state in $a(i)$ and then consult the inverse state table to find each t such that $\delta(t,a)$ is in $a(i)$. Each time a new t is found, the block containing t is located and t placed on a list of states to be split off from the block. The block is then placed on a list of blocks which have been refined if it is not already on the list. Finally we go down the list of blocks which have been refined and actually partition them. The number of blocks we must look at must be less than the number of state transitions on input a terminating on states in $B(i)$. The time necessary to actually partition a block is proportional to the number of states to be split off. When the number is summed over all blocks which are partitioned it adds up to the number of state transitions on input a terminating on states in $B(i)$.) Let k be the constant of proportionality.

Consider the time spent in the loop of Step 5 through 8 for a given input symbol a . Assume that at step 5 the blocks of the partition are $B(1), B(2), \dots, B(m)$ and that $L(a) = \{i_1, i_2, \dots, i_r\}$. Let $\{i_{r+1}, i_{r+2}, \dots, i_m\} = \{1, 2, \dots, m\} - L(a)$. The claim is made that the total time spent on traversals of the loop for which input symbol a is selected in Step 5 until the program terminates is bounded by

$$T = k \left(\sum_{j=1}^r a_{i_j} \log a_{i_j} + \sum_{j=r+1}^m a_{i_j} \log(a_{i_j}/2) \right).$$

Clearly the bound is valid if the algorithm has terminated. If the loop is traversed for an input symbol other than a , then the time spent is not included in T . However, since blocks get split and the set $L(a)$ modified we must show that the new value of T , call it \hat{T} , is less than or equal to the old value of T . If a block whose index is in $L(a)$ is partitioned, then a term of the form $b \log b$ is replaced by the expression $c \log c + (b-c) \log(b-c)$ which decreases the value of T . If a block whose index is not in $L(a)$ is partitioned, then a term of the form $b \log b/2$ is replaced by the expression

$$c \log c + (b-c) \log(b-c)/2$$

where $c \leq b-c$. Since $c \leq b/2$ and $(b-c)/2 \leq b/2$,

$$c \log c + (b-c) \log(b-c)/2 \leq c \log b/2 + (b-c) \log b/2 \\ \leq b \log b/2.$$

either case \hat{T} is less than T . Finally, assume $r \neq 0$ and a and some l in $L(a)$ has been

selected at Step 5. As we showed earlier, the time around the loop is bounded by ka_ℓ . Thus by induction, the total time is bounded by

$$k[a_\ell + a_\ell \log(a_\ell/2)] + \sum_{\substack{j=1 \\ j \neq \ell}}^r a_{i_j} \log a_{i_j} + \sum_{j=r+1}^m a_{i_j} \log(a_{i_j}/2) \quad 1.$$

We must show that this expression is less than or equal to T . That is, we need show

$$a_\ell + a_\ell \log a_\ell/2 \leq a_\ell \log a_\ell.$$

Clearly $a_\ell + a_\ell \log a_\ell/2 = a_\ell(\log a_\ell/2 + \log 2) = a_\ell \log a_\ell$. This completes the proof of the claim.

The first time Step 5 is executed the formula for T is bounded by $k \log n$. Multiply by the number of input symbols and adding in the time for Steps 1 through 4 yields a total bound proportional to $n \log n$.

Experimental results and conclusions

In order to obtain timing information, the algorithm was applied to two classes of finite automata. Automata in the first class are given by $A(n) = (\{1, 2, \dots, n\}, \{0, 1\}, \delta, \{1\})$ where $\delta(1, 0) = \delta(1, 1) = 1$ and $\delta(i, 0) = i-1$ and $\delta(i, 1) = i$ for $2 \leq i \leq n$. Automata in the second class are given (for even n) by $B(n) = (\{1, 2, \dots, n\}, \{0, 1\}, \delta, \{i \mid 1 \leq i \leq n/2\})$ where $\delta(i, 0) = \delta(i, 1) = n/2 + 2i-1$ and $\delta(n/4 + i, 0) = \delta(n/4 + i, 1) = 2i-1$ for $1 \leq i \leq n/4$ and $\delta(n/2 + i, 0) = \delta(n/2 + i, 1) = 2i-1$ for $n/2 < i \leq n$. The running times on an IBM 360/67 for the two classes are listed in Table 1.

n	A(n)	B(n)
100	$37 \frac{2}{60}$	$\frac{2}{3}$
1000	$5 \frac{50}{60}$	$6 \frac{3}{4}$
2003	$11 \frac{38}{60}$	$13 \frac{2}{3}$

Table 1. time in seconds

Note that $A(n)$ is the example which required n^2 steps for previous algorithms.

Our algorithm is particularly suited for $A(n)$ and a detailed analysis shows that the running time should grow linearly-with the number of states as the experimental evidence indicates. The worst case for our algorithm is typified by $B(n)$ in which blocks are always partitioned equally. The running time for $B(n)$ should grow as $n \log n$ for both the current algorithm and for previously published algorithms. The results seem to indicate that the algorithm is practical for minimizing states in finite automata (or testing equivalence of finite automata) of up to several thousand states.

References

- Harrison, M. A., Introduction to Switching and Automata Theory, McGraw-Hill, New York, 1965.
- McCluskey, E. J., Introduction to the Theory of Switching Circuits, McGraw-Hill, New York, 1965.



```

0001 --
0002 --
0003 --
0004 1-
0005 --
0006 --
0007 2-
0008 --
0009 --
0010 --
0011 --
0012 --
0013 --
0014 --
0015 --
0016 --
0017 --
0018 --
0019 --
0020 --
0021 --
0022 --
0023 --
0024 2-
0025 --
0026 --
0027 --
0028 --
0029 --
0030 --
0031 3-
0032 --
0033 3-
0034 --
0035 --
0036 --
0037 --
0038 --
0039 3-
0040 --
0041 3-
0042 --
0043 --
0044 --
0045 --
0046 --
0047 3-
0048 --
0049 2-
0050 --
0051 --
0052 --
0053 --
0054 --
0055 3-
0056 --
0057 2-
0058 --

```

```

CONVERTER *****
* ALSO LISTS TO MINIMIZE STATES IN FINITE AUTOMATON
*****
BEGIN
  INTEGER N;
  PROC(A);
  BEGIN
    INTEGER ARRAY TEXTZERO, NEXTNONE, PREVONZERO, PREVONONE(1:1:N);
    INTEGER ARRAY FINAL(1:1:N);
    INTEGER ARRAY DATA, NEXT(1:1:6*N);
    INTEGER ARRAY STATNEXT, STATELAST, GENEXACT, UNELAST, ZERONEXT,
      ZEROLAST, SPLIT(1:1:2*N);
    INTEGER ARRAY BLOCK, NUMINBLOCK, NUMINSPLIT(1:1:N);
    INTEGER ARRAY BLOCKSPLIT(1:1:N);
    INTEGER ARRAY NUMZERO, NUMONE(1:1:N);
    INTEGER ARRAY BLOCKLISTZERO, BLOCKLISTONE(1:1:N);
    INTEGER ARRAY ZEROSTR, ONESTR(1:1:N);
    INTEGER BLOCKLISTZERO, BLOCKLISTONE;
    INTEGER FORC, CELL;
    INTEGER LIST;
    INTEGER SPLITLOCKS;
    INTEGER K,J, STATE, LASTSTATE, FREEBLOCK, SPLIT;
    PROCEDURE ADD(INTEGER VALUE RESULT X; INTEGER VALUE Y);
      BEGIN
        INTEGER TEMP;
        TEMP:=FREE;
        FREE:=NEXT(FREE);
        NEXT(TEMP):=X;
        X:=TEMP;
        DATA(X):=Y;
      END;
    PROCEDURE REMOVEFROM(INTEGER VALUE RESULT X);
      BEGIN
        INTEGER TEMP;
        TEMP:=FREE;
        FREE:=NEXT(FREE);
        NEXT(TEMP):=X;
        X:=NEXT(X);
        NEXT(FREE):=TEMP;
      END;
    PROCEDURE INSERTSTATE(INTEGER VALUE X,Y);
      BEGIN
        STATENEXT(N+Y):=STATENEXT(X);
        STATELAST(N+Y):=X;
        IF (STATENEXT(X)=0) THEN
          STATELAST(STATENEXT(X)):=N+Y;
          STATENEXT(X):=N+Y;
        END;
      PROCEDURE INSERTZERO(INTEGER VALUE X,Y);
      BEGIN
        ZERONEXT(N+Y):=ZERONEXT(X);
        ZEROLAST(N+Y):=X;
        IF (ZERONEXT(X)=0) THEN
          ZEROLIST(ZERONEXT(X)):=N+Y;
          ZERONEXT(X):=N+Y;
        END;
      PROCEDURE INSERTONE(INTEGER VALUE X,Y);
      BEGIN
        ONESTR(N+Y):=ONESTR(X);

```

```

0050-  CNELAST(N+Y):=X;
0060-  IF (CNNEXT(X)=0) THEN
0061-  CNELAST(CNEXT(X)):=N+Y;
0062-  CNNEXT(X):=N+Y;
0063-  END;
0064-  PROCEDURE INITIALIZE;
0065-  BEGIN
0066-  *****
0067-  * INITIALIZE *****
0068-  *****
0069-  * *****
0070-  FOR I:=1 UNTIL 2*N DO
0071-  BEGIN
0072-  STATEXT(I):=STATELAST(I):=0;
0073-  CNEAEXT(I):=CNELAST(I):=0;
0074-  ZERONEXT(I):=ZEROLAST(I):=0;
0075-  END;
0076-  FOR I:=1 UNTIL N DO
0077-  BEGIN
0078-  PREVNZERC(I):=PREVNCNE(I):=0;
0079-  NUMZERC(I):=NUMCNE(I):=0;
0080-  SPLIT(I):=BLCKSPLIT(I):=0;
0081-  NUMRELCCK(I):=NUMINSPLIT(I):=0;
0082-  CNBLCKLISTZERC(I):=CNBLCKLISTOIE(I):=0;
0083-  NZERT(I):=NCNETR(I):=0;
0084-  END;
0085-  FOR I:=1 UNTIL 6*N DO NEXT(I):=I+1;
0086-  NEXT(6*N):=0;
0087-  BLCKLISTZERC:=BLCKLISTONE:=0;
0088-  ERCE:=1;
0089-  SPLITELCCK:=0;
0090-  WRITE("STATE TABLE");
0091-  WRITE(" STATE INPUT C INPUT I
OUTPUT");
0092-  FOR I:=1 UNTIL N DO
0093-  BEGIN
0094-  READ(NEXTNZERO(I),NEXTONCNE(I),FINAL(I));
0095-  WRITE(I,NEXTNZERC(I),NEXTONCNE(I),FINAL(I));
0096-  END;
0097-  * *****
0098-  * INVERT STATE TABLE *****
0099-  *****
0100-  FOR I:=1 UNTIL N DO
0101-  BEGIN
0102-  ACC(PREVNZERC(NEXTNZERO(I)),I);
0103-  NZERT(CDATA(PREVNZERC(NEXTNZERO(I)))):=
0104-  NZERT(CDATA(PREVNZERC(NEXTNZERO(I)))+1);
0105-  ACC(PREVNCNE(NEXTONCNE(I)),I);
0106-  NCNETR(CDATA(PREVNCNE(NEXTONCNE(I)))):=
0107-  NCNETR(CDATA(PREVNCNE(NEXTONCNE(I)))+1);
0108-  END;
0109-  * *****
0110-  * SET UP INITIAL BLCKS *****
0111-  *****
0112-  FOR I:=1 UNTIL N DO
0113-  BEGIN
0114-  IF FINAL(I)=1 THEN
0115-  BEGIN
0116-  INSERTSTATE(I,I);

```



```

0175 - J:=DATA(BLCKLISTZERO);
0176 - REMOVEFROM(BLCKLISTZERO);
0177 - BLCKLISTZERO(J):=0;
0178 - GOTO DIVZERO;
0179 - 3
0180 - ENCL
0181 - ELSE IF BLCKLISTCNE#0 THEN
0182 - BEGIN
0183 - J:=DATA(BLCKLISTCNE);
0184 - REMOVEFROM(BLCKLISTCNE);
0185 - BLCKLISTCNE(J):=0;
0186 - 3
0187 - ENCL
0188 - GOTO DIVFINISHED;
0189 - STATE:=ZERONEXT(J);
0190 - 3
0191 - WHILE STATE#0 DO
0192 - BEGIN
0193 - CELL:=PREVNZERO(STATE-N);
0194 - WHILE CELL#0 DO
0195 - BEGIN
0196 - LASTSTATE:=DATA(CELL);
0197 - K:=BLCKK(LASTSTATE);
0198 - ADD(SPLIT(K),LASTSTATE);
0199 - IF "LGCKSPLIT(K)=0 THEN ADD(SPLITBLOCKS,K);
0200 - NUMINSPLIT(K):=NUMINSPLIT(K)+1;
0201 - BLCKSPLIT(K)=1;
0202 - CELL:=NEXT(CELL);
0203 - 4
0204 - END;
0205 - STATE:=ZERONEXT(STATE);
0206 - 3
0207 - ENCL
0208 - GOTO RETURN;
0209 - STATE:=CHENEXT(J);
0210 - 3
0211 - WHILE STATE#0 DO
0212 - BEGIN
0213 - CELL:=PREVZNONE(STATE-N);
0214 - WHILE CELL#0 DO
0215 - BEGIN
0216 - LASTSTATE:=DATA(CELL);
0217 - K:=BLCKK(LASTSTATE);
0218 - ADD(SPLIT(K),LASTSTATE);
0219 - IF "BLCKSPLIT(K)=0 THEN ADD(SPLITBLOCKS,K);
0220 - NUMINSPLIT(K):=NUMINSPLIT(K)+1;
0221 - BLCKSPLIT(K)=1;
0222 - CELL:=NEXT(CELL);
0223 - 4
0224 - END;
0225 - STATE:=CHENEXT(STATE);
0226 - 3
0227 - ENCL
0228 - COMMENT*****
0229 - * REFINE BLCKS *****
0230 - * *****
0231 - RETURN: I F(SPLITBLOCKS#0) THEN GOTO CREPEAT;
0232 - RPLIT:=DATA(SPLITBLOCKS);
0233 - REMOVEFROM(SPLITBLOCKS);
0234 - BLCKSPLIT(RSPLIT)=0;
0235 - I F NUMINBLOCK(RSPLIT)=NUMINSPLIT(RSPLIT) THEN
0236 - BEGIN
0237 - NUMINSPLIT(RSPLIT)=0;
0238 - WHILE (SPLIT(RSPLIT)#0) DO REMOVEFROM(SPLIT(RSPLIT));
0239 - GOTO RETURN;
0240 - 3
0241 - ENCL

```

```

0233 -3      END;
0234 - -      NUMINBLOCK(BSPPLIT):=NUMINBLOCK(BSPPLIT)-NUMINSPLIT(BSPPLIT);
0235 --      NUMINBLOCK(FREEBLOCK):=NUMINSPLIT(BSPPLIT);
0236 - -      NUMINSPLIT(BSPPLIT)=0;
0237 - -      WHILE SPLIT(BSPPLIT)≠0 DO
0238 3--      BEGIN
0239 - -          STATE:=DATA(SPLIT(BSPPLIT));
0240 - -          REMOVEFRM(SPLIT(BSPPLIT));
0241 - -          STATENEXT(STATLAST(N+STATE)):=STATENEXT(N+STATE);
0242 - -          IF STATENEXT(N+STATE)≠C THEN
0243 - -              STATLAST(STATENEXT(N+STATE)):=STATELAST(N+STATE);
0244 - -          INSERTSTATE(FREEBLOCK,STATE);
0245 --      BLOCK(STATE):=FREEBLOCK;
0246 --      IF PREVNZERC(STATE)≠0 THEN
0247 4--      BEGIN
0248 - -          ZERONEXT(ZEROLAST(N+STATE)):=ZERONEXT(N+STATE);
0249 - -          IF ZERONEXT(N+STATE)≠C THEN
0250 - -              ZEROLAST(ZERONEXT(N+STATE)):=ZEROLAST(N+STATE);
0251 - -          INSERTZER0(FREEBLOCK,STATE);
0252 - -          ALZERC(BSPPLIT):=NUMZERC(BSPPLIT)-AZERC(BSPPLIT);
0253 - -          NUMZER0(FREEBLOCK):=NUMZER0(FREEBLOCK)+AZERC(STATE);
0254 - 4      END;
0255 - -      IF PREVNONE(STATE)≠0 THEN
0256 4--      BEGIN
0257 - -          ONENEXT(CNELAST(N+STATE)):=ONENEXT(N+STATE);
0258 - -          IF CNEXT(N+STATE)≠C THEN
0259 - -              CRELAST(CNEXT(N+STATE)):=CNELAST(N+STATE);
0260 - -          INSERTC(FREEBLOCK,STATE);
0261 - -          NUMONE(BSPPLIT):=NUMONE(BSPPLIT)-NUMETR(STATE);
0262 - -          NUMONE(FREEBLOCK):=NUMONE(FREEBLOCK)+NUMETR(STATE);
0263 - 4      END;
0264 - 3      END;
0265 - -      #      ADD NFA BLOCK TO BLOCKLIST
0266 - -      *****
0267 --      COMMENT*****
0268 --      *****
0269 - -      IF (CNUMBLOCKLISTZERC(BSPPLIT)≠1) AND (NUMZERC(BSPPLIT)>0) AND
3270 3--      (NUMZERC(BSPPLIT)<NUMZER0(FREEBLOCK)) THEN
0271 - -      BEGIN
0272 - -          ADD(BLOCKLISTZERC,BSPPLIT);
0273 - 3      CNUMBLOCKLISTZERC(BSPPLIT)=1;
0274 - -      ELSE
0275 - -      BEGIN
0276 --      ADD(BLOCKLISTZERO,FREEBLOCK);
0277 --      CNUMBLOCKLISTZERC(FREEBLOCK)=1;
0278 - ?      END;
0279 - -      IF (CNUMBLOCKLISTONE(BSPPLIT)≠1) AND (NUMONE(BSPPLIT)>C) AND
d/f - -      (NUMONE(BSPPLIT)<NUMONE(FREEBLOCK)) THEN
3281 3--      BEGIN
0282 - -          ADD(BLOCKLISTONE,BSPPLIT);
0283 - -          CNUMBLOCKLISTONE(BSPPLIT)=1;
0284 - 3      END;
0285 - -      ELSE
0286 3--      BEGIN
0287 - -          ADD(BLOCKLISTONE,FREEBLOCK);
0288 - -          CNUMBLOCKLISTONE(FREEBLOCK)=1;
0289 - 3      END;
0290 - -      FREEBLOCK:=FREEBLOCK+1;

```

```

0291 --      GO TO RETURN;
0292 --      COMMENT*****
0293 --      *      OUTPUT
0294 --      *****
0245 --      FINISHED:FOR I:=1 UNTIL N DO
0296 3--      BEGIN
0297 --          CELL:=STATENEXT(I);
0298 --          IF CELL=0 THEN GO TO EXIT;
0299 --          WRITE("BLOCK",I);
0300 --          WRITE(CELL-N);
0301 --          CELL:=STATENEXT(CELL);
0302 --          WHILE CELL=0 DO
0303 4--              BEGIN
0304 --                  WRITECN(CELL-N);
0305 --                  CELL:=STATENEXT(CELL);
0306 --              END;
0307 --          END;
0308 --      END;
0309 -- 1  END.

```

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